## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Bhattacharya et al.

<sub>6</sub>, , }

09/896,059



METHOD FOR AUTOMATED DESIGN OF INTEGRATED CIRCUITS WITH TARGETED QUALITY OBJECTIVES USING DYNAMCALLY GENERATED BUILDING BLOCKS

Filed:

June 29, 2001

**Examiner:** 

A. M. Thompson

Art Unit:

2825

Confirmation No.:

9475

Customer No.:

27623

Attorney Docket No.: 162.7107USU

## REQUEST FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This is a petition for extension of time for a total period of two (2) months. Based on the extension requested, the extended period for response will expire on July 10, 2006, 2006 (since July 9, 2006 falls on a Sunday).

A check in the amount of \$225.00 (small entity) is enclosed with this Petition. The Commissioner is hereby authorized to charge any fees or credit any overpayment, to Deposit Account No. 01-0467. A duplicate copy of this Request is enclosed.

Date: June 15, 2006

Paul D. Greelev Reg. No. 31,019

Attorney for Applicant(s)

Respectfully submitted.

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